

1. Description

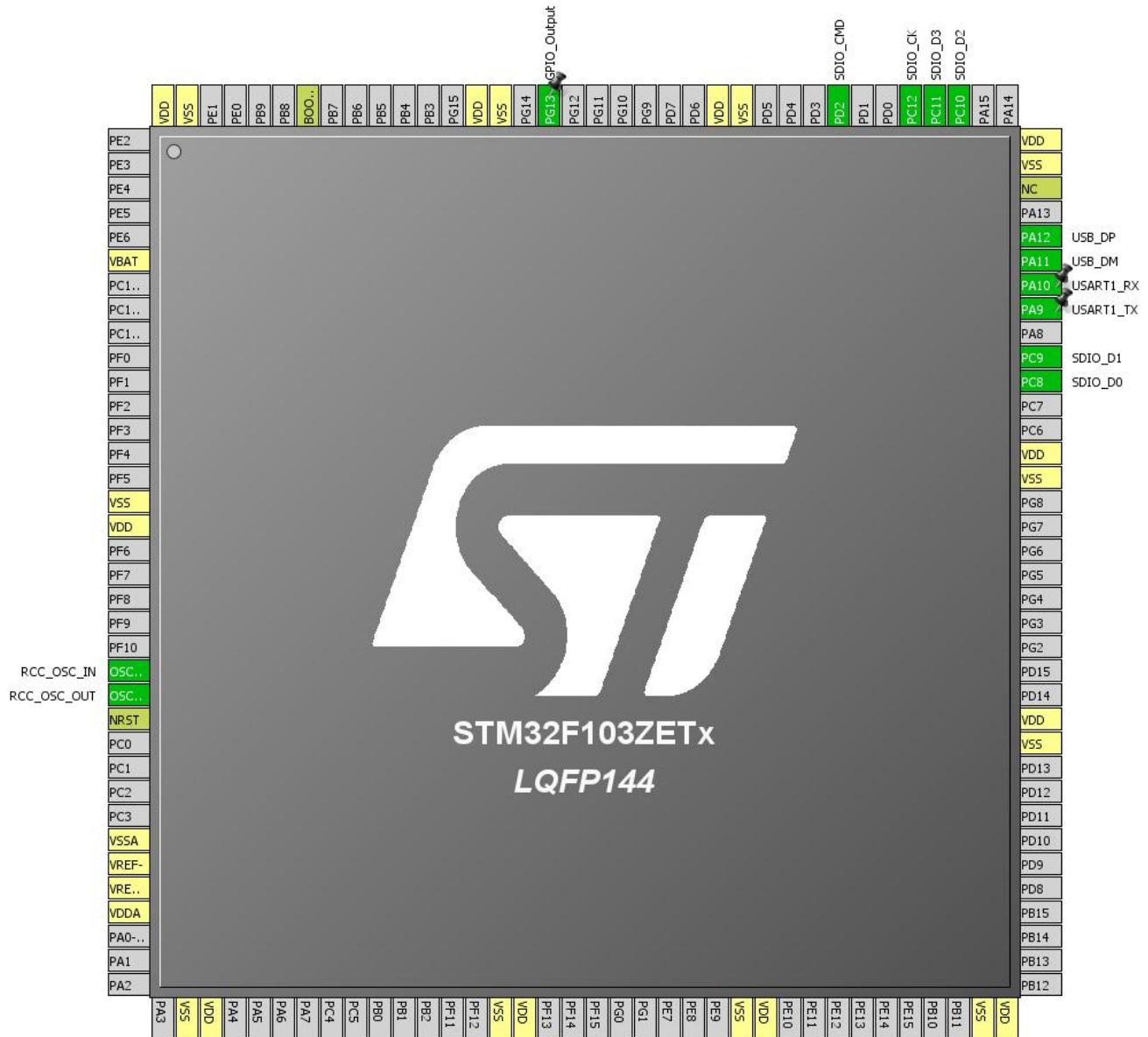
1.1. Project

Project Name	YS-F1Pro
Board Name	YS-F1Pro
Generated with:	STM32CubeMX 4.14.0
Date	06/11/2016

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103ZETx
MCU Package	LQFP144
MCU Pin number	144

2. Pinout Configuration



3. Pins Configuration

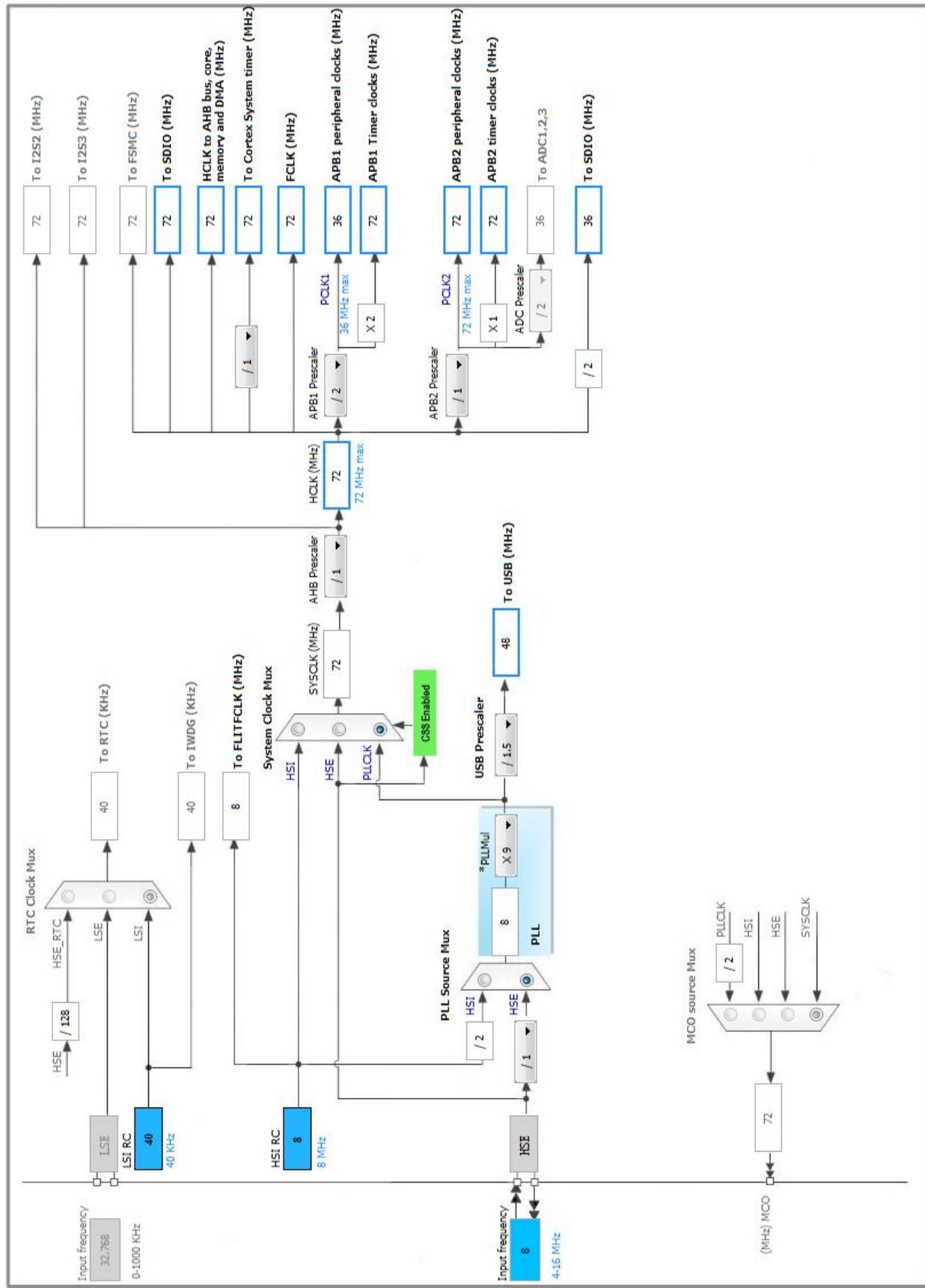
Pin Number LQFP144	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
16	VSS	Power		
17	VDD	Power		
23	OSC_IN	I/O	RCC_OSC_IN	
24	OSC_OUT	I/O	RCC_OSC_OUT	
25	NRST	Reset		
30	VSSA	Power		
31	VREF-	Power		
32	VREF+	Power		
33	VDDA	Power		
38	VSS	Power		
39	VDD	Power		
51	VSS	Power		
52	VDD	Power		
61	VSS	Power		
62	VDD	Power		
71	VSS	Power		
72	VDD	Power		
83	VSS	Power		
84	VDD	Power		
94	VSS	Power		
95	VDD	Power		
98	PC8	I/O	SDIO_D0	
99	PC9	I/O	SDIO_D1	
101	PA9	I/O	USART1_TX	
102	PA10	I/O	USART1_RX	
103	PA11	I/O	USB_DM	
104	PA12	I/O	USB_DP	
106	NC	NC		
107	VSS	Power		
108	VDD	Power		
111	PC10	I/O	SDIO_D2	
112	PC11	I/O	SDIO_D3	
113	PC12	I/O	SDIO_CK	
116	PD2	I/O	SDIO_CMD	
120	VSS	Power		

YS-F1Pro Project
Configuration Report

Pin Number LQFP144	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
121	VDD	Power		
128	PG13 *	I/O	GPIO_Output	
130	VSS	Power		
131	VDD	Power		
138	BOOT0	Boot		
143	VSS	Power		
144	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

5.1.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
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5.2. SDIO

Mode: SD 4 bits Wide bus

5.2.1. Parameter Settings:

SDIO parameters:

SDIOCLK clock divide factor	2 *
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5.3. SYS

Timebase Source: SysTick

5.4. USART1

Mode: Asynchronous

5.4.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)

Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

5.5. USB

mode: Device (FS)

5.5.1. Parameter Settings:

Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	64 Bytes *

Power Parameters:

Low Power	Disabled
Link Power Management	Disabled
Battery Charging	Disabled

5.6. USB_DEVICE

Class For FS IP: Mass Storage Class

5.6.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	0x100 *
USBD_SUPPORT_USER_STRING (Enable user string descriptor)	Disabled
USBD_SELF_POWERED (Enabled self power)	Enabled
USBD_DEBUG_LEVEL (USBD Debug Level)	0: No debug message

Class Parameters:

MSC_MEDIA_PACKET (Media I/O buffer Size)	512
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5.6.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier)	1155
LANGID_STRING (Language Identifier)	English(United States)
MANUFACTURER_STRING (Manufacturer Identifier)	STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier)	22314
PRODUCT_STRING (Product Identifier)	STM32 Mass Storage
SERIALNUMBER_STRING (Serial number)	0000000001A
CONFIGURATION_STRING (Configuration Identifier)	MSC Config
INTERFACE_STRING (Interface Identifier)	MSC Interface

* User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SDIO	PC8	SDIO_D0	Alternate Function Push Pull	n/a	High	
	PC9	SDIO_D1	Alternate Function Push Pull	n/a	High	
	PC10	SDIO_D2	Alternate Function Push Pull	n/a	High	
	PC11	SDIO_D3	Alternate Function Push Pull	n/a	High	
	PC12	SDIO_CK	Alternate Function Push Pull	n/a	High	
	PD2	SDIO_CMD	Alternate Function Push Pull	n/a	High	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
GPIO	PG13	GPIO_Output	Output Open Drain *	n/a	High *	

6.2. DMA configuration

DMA request	Stream	Direction	Priority
SDIO	DMA2_Channel4	Peripheral To Memory	High *

SDIO: DMA2_Channel4 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: **Word ***
Memory Data Width: Word

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
Debug monitor	true	0	0
System tick timer	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
SDIO global interrupt	true	0	1
DMA2 channel4 and channel5 global interrupts	true	0	0
PVD interrupt through EXTI line 16		unused	
Flash global interrupt		unused	
RCC global interrupt		unused	
USB high priority or CAN TX interrupts		unused	
USART1 global interrupt		unused	

* User modified value

7. Power Plugin report

7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103ZETx
Datasheet	14611_Rev11

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	YS-F1Pro
Project Folder	E:\2. (HAL)\1. (HAL)\YSF1_HAL-059. USB-SDU
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F1 V1.3.1

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No